



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Result of II B.Tech I SEMESTER (R16) EXAMINATIONS, FEB.-2022

College name: GAYATHRI VIDYA PARISHAD COLL OF ENGG FOR WOMEN, VISAKHAPATNA:JG

Htno	Subcode	Subname	Grade	Credits
16JG1A0203	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	D	3
16JG1A0203	R1621022	ELECTRICAL MACHINES-I	D	3
16JG1A0203	R1621023	BASIC ELECTRONICS AND DEVICES	F	0
16JG1A0203	R1621024	ELECTRO MAGNETIC FIELDS	D	3
16JG1A0205	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	ABSENT	0
16JG1A0206	R1621022	ELECTRICAL MACHINES-I	D	3
16JG1A0207	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	D	3
16JG1A0207	R1621022	ELECTRICAL MACHINES-I	F	0
16JG1A0207	R1621023	BASIC ELECTRONICS AND DEVICES	D	3
16JG1A0207	R1621024	ELECTRO MAGNETIC FIELDS	D	3
16JG1A0207	R1621025	THERMAL AND HYDRO PRIME MOVERS	F	0
16JG1A0207	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
16JG1A0208	R1621024	ELECTRO MAGNETIC FIELDS	F	0
16JG1A0209	R1621022	ELECTRICAL MACHINES-I	C	3
16JG1A0220	R1621023	BASIC ELECTRONICS AND DEVICES	F	0
16JG1A0222	R1621022	ELECTRICAL MACHINES-I	D	3
16JG1A0232	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	F	0
16JG1A0232	R1621022	ELECTRICAL MACHINES-I	F	0
16JG1A0232	R1621023	BASIC ELECTRONICS AND DEVICES	F	0
16JG1A0416	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16JG1A0416	R1621043	SIGNALS AND SYSTEMS	F	0
16JG1A0493	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	ABSENT	0
16JG1A0493	R1621041	ELECTRONIC DEVICES AND CIRCUITS	ABSENT	0
16JG1A0493	R1621042	SWITCHING THEORY AND LOGIC DESIGN	ABSENT	0
16JG1A0493	R1621043	SIGNALS AND SYSTEMS	ABSENT	0
16JG1A0493	R1621044	NETWORK ANALYSIS	ABSENT	0
16JG1A0516	R1621051	STATISTICS WITH R PROGRAMMING	ABSENT	0
16JG1A0516	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	ABSENT	0
16JG1A0516	R1621053	DIGITAL LOGIC DESIGN	ABSENT	0
16JG1A0516	R1621056	COMPUTER GRAPHICS	ABSENT	0
16JG1A0545	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16JG1A0545	R1621053	DIGITAL LOGIC DESIGN	F	0
16JG1A0545	R1621055	DATA STRUCTURES THROUGH C++	D	3
16JG1A0553	R1621053	DIGITAL LOGIC DESIGN	D	3
16JG1A1206	R1621051	STATISTICS WITH R PROGRAMMING	ABSENT	0
16JG1A1206	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	ABSENT	0
16JG1A1206	R1621053	DIGITAL LOGIC DESIGN	ABSENT	0
16JG1A1206	R1621054	PYTHON PROGRAMMING	ABSENT	0
16JG1A1206	R1621055	DATA STRUCTURES THROUGH C++	ABSENT	0
16JG1A1206	R1621057	DATA STRUCTURES THROUGH C++ LAB	ABSENT	0
16JG1A1206	R1621058	PYTHON PROGRAMMING LAB	ABSENT	0
16JG1A1206	R1621121	SOFTWARE ENGINEERING	ABSENT	0
16JG1A1218	R1621053	DIGITAL LOGIC DESIGN	F	0
16JG1A1218	R1621054	PYTHON PROGRAMMING	F	0
16JG1A1218	R1621121	SOFTWARE ENGINEERING	F	0

Htno	Subcode	Subname	Grade	Credits
16JG1A1256	R1621051	STATISTICS WITH R PROGRAMMING	D	3
17JG1A0202	R1621022	ELECTRICAL MACHINES-I	ABSENT	0
17JG1A0202	R1621023	BASIC ELECTRONICS AND DEVICES	ABSENT	0
17JG1A0202	R1621024	ELECTRO MAGNETIC FIELDS	ABSENT	0
17JG1A0202	R1621025	THERMAL AND HYDRO PRIME MOVERS	ABSENT	0
17JG1A0221	R1621022	ELECTRICAL MACHINES-I	F	0
17JG1A0232	R1621022	ELECTRICAL MACHINES-I	F	0
17JG1A0461	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	A	3
17JG1A0495	R1621043	SIGNALS AND SYSTEMS	ABSENT	0
17JG1A0495	R1621044	NETWORK ANALYSIS	ABSENT	0
17JG1A0495	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	ABSENT	0
17JG1A0499	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
17JG1A0499	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	ABSENT	0
17JG1A04A6	R1621043	SIGNALS AND SYSTEMS	D	3
17JG1A0584	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
17JG1A0584	R1621053	DIGITAL LOGIC DESIGN	F	0
17JG1A0584	R1621054	PYTHON PROGRAMMING	F	0
17JG1A0584	R1621056	COMPUTER GRAPHICS	ABSENT	0
17JG1A05A3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
17JG1A05A3	R1621054	PYTHON PROGRAMMING	F	0
17JG1A1238	R1621053	DIGITAL LOGIC DESIGN	ABSENT	0
17JG1A1241	R1621053	DIGITAL LOGIC DESIGN	F	0
17JG1A1246	R1621051	STATISTICS WITH R PROGRAMMING	ABSENT	0
17JG1A1246	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	ABSENT	0
17JG1A1246	R1621053	DIGITAL LOGIC DESIGN	ABSENT	0
18JG1A0202	R1621022	ELECTRICAL MACHINES-I	F	0
18JG1A0207	R1621022	ELECTRICAL MACHINES-I	D	3
18JG1A0208	R1621022	ELECTRICAL MACHINES-I	D	3
18JG1A0213	R1621022	ELECTRICAL MACHINES-I	C	3
18JG1A0221	R1621022	ELECTRICAL MACHINES-I	F	0
18JG1A0221	R1621025	THERMAL AND HYDRO PRIME MOVERS	F	0
18JG1A0223	R1621022	ELECTRICAL MACHINES-I	F	0
18JG1A0413	R1621044	NETWORK ANALYSIS	D	3
18JG1A0417	R1621041	ELECTRONIC DEVICES AND CIRCUITS	ABSENT	0
18JG1A0417	R1621043	SIGNALS AND SYSTEMS	ABSENT	0
18JG1A0417	R1621044	NETWORK ANALYSIS	ABSENT	0
18JG1A0419	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18JG1A0419	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18JG1A0419	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18JG1A0419	R1621043	SIGNALS AND SYSTEMS	F	0
18JG1A0419	R1621044	NETWORK ANALYSIS	F	0
18JG1A0419	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0433	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18JG1A0433	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0444	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18JG1A0450	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18JG1A0450	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0452	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18JG1A0452	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18JG1A0452	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0455	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3

Htno	Subcode	Subname	Grade	Credits
18JG1A0462	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	ABSENT	0
18JG1A0462	R1621041	ELECTRONIC DEVICES AND CIRCUITS	ABSENT	0
18JG1A0462	R1621042	SWITCHING THEORY AND LOGIC DESIGN	ABSENT	0
18JG1A0472	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18JG1A0478	R1621044	NETWORK ANALYSIS	D	3
18JG1A04A5	R1621044	NETWORK ANALYSIS	F	0
18JG1A04A5	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A04A7	R1621043	SIGNALS AND SYSTEMS	ABSENT	0
18JG1A04A7	R1621044	NETWORK ANALYSIS	ABSENT	0
18JG1A04A7	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	ABSENT	0
18JG1A0502	R1621053	DIGITAL LOGIC DESIGN	D	3
18JG1A0535	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18JG1A0535	R1621054	PYTHON PROGRAMMING	F	0
18JG1A0535	R1621055	DATA STRUCTURES THROUGH C++	F	0
18JG1A0535	R1621056	COMPUTER GRAPHICS	D	3
18JG1A0548	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18JG1A0548	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18JG1A0548	R1621056	COMPUTER GRAPHICS	C	3
18JG1A1204	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18JG1A1212	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	ABSENT	0
18JG1A1212	R1621053	DIGITAL LOGIC DESIGN	F	0
18JG1A1212	R1621054	PYTHON PROGRAMMING	ABSENT	0
18JG1A1225	R1621055	DATA STRUCTURES THROUGH C++	C	3
18JG5A0404	R1621043	SIGNALS AND SYSTEMS	F	0
18JG5A0407	R1621043	SIGNALS AND SYSTEMS	ABSENT	0
19JG5A0404	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	ABSENT	0
19JG5A0506	R1621051	STATISTICS WITH R PROGRAMMING	C	3

**Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is : 24-05-2022]

** Note:**

* -1 in the filed of externals indicates student is absent for the respective subject.

* -2 in the filed of externals indicates student result Withheld for the respective subject.

* -3 in the filed of externals indicates student involved in Malpractice for the respective subject.



Date:17.05.2022

Controller of Examinations